

Amendments to the Claims:

1. (currently amended) A variable attenuator comprising:  
a first attenuator for attenuating a RF signal according to a fixed voltage;  
a second attenuator connected in parallel to the first attenuator and attenuating the RF signal according to a control voltage for determining an attenuation mode; and  
a first impedance matching unit for maintaining input/output impedance matching of the second attenuator,  
wherein the second attenuator comprises:  
a third capacitor connected in parallel to an input terminal of the first attenuator,  
a first short key diode for attenuating the RF signal transmitted through the third capacitor, and  
a second short key diode connected in parallel to an output terminal of the first attenuator.
2. (currently amended) The attenuator of claim 1, further comprising:  
a second impedance matching unit for maintaining input/output impedance matching of the first attenuator.
3. (currently amended) The attenuator of claim 1, further comprising:  
a first capacitor for removing serial current elements from an input RF signal and for providing the RF signal to the first attenuator; and  
a second capacitor for removing serial current elements from an output signal of the first and second attenuators.
4. (original) The attenuator of claim 1, wherein the first attenuator comprises a short key diode.
5. (original) The attenuator of claim 1, wherein the fixed voltage is a power voltage divided by a certain level.

6. (original) The attenuator of claim 1, wherein the RF signal flows to the first attenuator in a low attenuation mode.
7. (original) The attenuator of claim 1, wherein the RF signal flows to the second attenuator in a high attenuation mode.
8. (original) The attenuator of claim 1, wherein the RF signal flows to the first and second attenuators in an intermediate attenuation mode.
- 9-11 (cancel)
12. (currently amended) The attenuator of claim [[11]] --1--, wherein the second attenuator comprises a first and a second resistance means connected in serial between anodes of the first and second short key diodes.
13. (original) The attenuator of claim 12, wherein the second attenuator comprises a control voltage input terminal is provided between the first and second resistance means.
14. (currently amended) A variable attenuator comprising:  
a first attenuator for attenuating a RF signal according to a first voltage;  
a first impedance matching unit for maintaining impedance matching of the first attenuator in a low attenuation mode;  
a second attenuator provided between input and output terminals of the first attenuator for attenuating the RF signal according to a control voltage for determining an attenuation mode,  
wherein the second attenuator comprises:  
a third capacitor connected in parallel to an input terminal of the first attenuator for cutting off a control voltage to the first attenuator;  
a first short key diode for attenuating the RF signal transmitted through the third capacitor;

a second short key diode connected in parallel to an output terminal of the first attenuator; and

first and second resistance means corrected to anodes of the first and second short key diodes for applying the control voltage; and

a second impedance matching unit for maintaining input/output impedance matching of the second attenuator in a high attenuation mode,

wherein the second impedance matching unit comprises at least two resistors provided between the second attenuator and a ground connection.

15. (original) The attenuator of claim 14, wherein the first and second impedance matching units have the same resistance.

16. (currently amended) The attenuator of claim 14, wherein the ~~second impedance matching unit comprises at least two resistors provided between the second attenuator and a ground connection and each~~ have a resistance is of 50Ω.

17. (original) The attenuator of claim 14, wherein the RF signal flows to the first attenuator in a low attenuation mode and flows to the second attenuator in a high attenuation mode.

18. (original) The attenuator of claim 14, wherein the RF signal flows to the first and second attenuators in an intermediate attenuation mode.

19. (cancel)

20. (currently amended) A variable attenuator comprising:

a first short key diode for attenuating a RF signal according to a first voltage;

a second short key diode, connected in parallel to an anode of the first short key diode for attenuating the RF signal, according to a control voltage;

a third short key diode connected in parallel to a cathode of the first short key diode for attenuating the RF signal, according to the control voltage;

a first resistor provided between a cathode of the second short key diode and a ground connection; and

a second resistor provided between a cathode of the third short key diode and the ground connection.

21. (original) The attenuator of claim 20, further comprising:

a capacitor connected to the anode of the first short key diode for cutting off a control voltage provided to the first short key diode.

22. (original) The attenuator of claim 20, further comprising:

third and fourth resistors for applying the control voltage to the anodes of the second and third short key diodes, respectively.

23. (original) The attenuator of claim 20, wherein the first and second resistors are approximately  $50\Omega$ .

24. (original) The attenuator of claim 20, wherein the RF signal flows to the first short key diode, in a low attenuation mode.

25. (original) The attenuator of claim 20, wherein the RF signal flows to the second and third short key diodes in a high attenuation mode.

26. (original) The attenuator of claim 20, wherein the RF signal flows to the first, second, and third short key diodes, in an intermediate attenuation mode.

27. (new) A variable attenuator comprising:

a first attenuator for attenuating a RF signal according to a fixed voltage;

a second attenuator connected in parallel to the first attenuator and attenuating the RF signal according to a control voltage for determining an attenuation mode; and

a first impedance matching unit for maintaining input/output impedance matching of the second attenuator,

wherein the second attenuator comprises:

- a first diode connected in parallel to an input terminal of the first attenuator; and
- a second diode connected in parallel to an output terminal of the first attenuator.

28. (new) The attenuator of claim 27, wherein the second attenuator comprises a third capacitor connected in parallel to an input terminal of the first attenuator.

29. (new) The attenuator of claim 28, wherein the first diode attenuates the RF signal transmitted through the third capacitor.